Claims

1. A system for the control of the generation of an on-screen display (OSD) on a display screen, and wherein, upon the deletion, change or movement of an area of a first on screen display, control means for the display continue to operate the system until a request to generate, add or otherwise alter the display of the said area is received whereupon the control means detects whether or not a Vsync signal for the display screen has occurred since the change to the area of the first display.

2. A control system according to claim 1 characterised in that if the Vsync signal has occurred the generation, addition or other alteration with regard to the part of the OSD occurs immediately, whereas if no Vsync signal has occurred then the control means delays the new operation until the Vsync signal has occurred.

3. A control system according to claim 1 characterised in that if the display data buffer memory of a part of the first display is changed, the system continues until a request to draw into the previously displayed data buffer of the part is received, at which point, if a Vsync signal has occurred in the intervening period of time the generation of said second display occurs.

4. A control system according to claim 1 characterised in that if a part of the first display is moved, the system continues until a request to draw into its display data buffer memory is received at which time the processing proceeds immediately if a Vsync signal has occurred since the movement of the part of the first display, otherwise the command to redraw the display waits for a Vsync signal to occur.

5 A system according to claim 1 characterised in that Typically, if a region of the first OSD is deleted, the system continues in operation until a request to create a new region is made and at that time, if a vsync signal has occurred since the deletion, the creation can take place immediately but, if not, the creation is delayed until the vertical sync signal occurs.

6 A system according to claim 1 characterised in that the system is controlled with regard to the occurrence of the vsync signal with respect to those changes in the OSD which would not cause an artefact to be crated on screen.

7 A system according to claim 1 characterised in that when the request for an alteration is made, the first OSD display continues to be displayed until the generation of the change occurs.